

Code No: H4303/R13

M. Tech. II Semester Regular/ Supplementary Examinations, July-2016

DIGITAL CONTROLLERS

(Common to PE, P&ID, PE&ED, PE&D, EM&D)

Time: 3 hours

Max. Marks: 60

Answer any FIVE Questions
All Questions Carry Equal Marks

- | | | |
|------|--|----|
| 1. a | Describe the Timer 0 of PIC 16C61 | 7 |
| b | What are the various possibilities of the oscillator connections in PIC Microcontrollers? How are those selected? How is the frequency of oscillations decided in each of these modes? | 5 |
| 2. a | Discuss overview and features of peripheral interface microcontrollers. | 6 |
| b | Explain PIC 16C71 Analog to digital converter. | 6 |
| 3. | Describe the various components of C2XX DSP core. | 12 |
| 4. | Explain different Memory addressing modes of C2XX DSP core. | 12 |
| 5. | What are the interrupt Control registers and explain? | 12 |
| 6. | Analyze the sequential steps for interrupt response within the Event manager module. | 12 |
| 7. | Describe the Sequencer configurations of ADC in the DSP. | 12 |
| 8. | Explain the different types of Field Programmable Gate Arrays. | 12 |
