

Code No: J6801/R16

M. Tech. II Semester Regular Examinations, May-2017

EMBEDDED SYSTEM DESIGN

(Common to VLSI&ES (68), ES&VLSI (48), VLSID &ES (77), ES &VLSID (81)
VLSI (57), VLSID (72), VLSI System Design (61), VLSI & Micro Electronics (76)
SSP(45), DIP(63), CE&SP(46), IP , C & SP (80)
Digital Systems & Computer Electronics (06)
DECS (38), ECE (70), DECE (37)
C&CE (49), C&C (39)

Time: 3 Hours

Max. Marks: 60

Answer any FIVE Questions
All Questions Carry Equal Marks

1. a Discuss the characteristics of an Embedded System. [6M]
b Explain about the processor in an Embedded System. [6M]
2. a What are the different types of ROMs that are used in Embedded boards? Explain. [6M]
b Explain about the interfacing of an I/O device with the Embedded board. [6M]
3. Explain in brief about Memory device drivers. [12M]
4. Describe the various ways of getting Embedded Software into the target system. [12M]
5. a What are the different processor architectures that should be considered in designing an Embedded System? [6M]
b Explain the use of Debugging tools in an Embedded System design. [6M]
6. a Discuss the various applications of an Embedded System. [6M]
b Write a brief note on Bus arbitration. [6M]
7. a Explain about Application Software with examples. [6M]
b Write a brief note on System Boot-Up. [6M]
8. Briefly explain about Power PC Processor based Embedded System design on Xilinx platform. [12M]
