

Roll No.

--	--	--	--	--	--	--	--	--	--

Total No. of Pages : 02

Total No. of Questions : 18

B.Tech.(CSE)/(IT) (2011 Onwards)**B.Tech.(3D Animation & Graphics) (2012 Onwards)****(Sem.-3)****COMPUTER ARCHITECTURE****Subject Code : BTCS-301****Paper ID : [A1123]****Time : 3 Hrs.****Max. Marks : 60****INSTRUCTION TO CANDIDATES :**

1. **SECTION-A** is **COMPULSORY** consisting of **TEN** questions carrying **TWO** marks each.
2. **SECTION-B** contains **FIVE** questions carrying **FIVE** marks each and students have to attempt any **FOUR** questions.
3. **SECTION-C** contains **THREE** questions carrying **TEN** marks each and students have to attempt any **TWO** questions.

SECTION-A**Answer briefly :**

1. Explain in brief about register transfer language.
2. Define Micro-operation with example.
3. Explain in brief about computer registers.
4. Define the term microinstruction and microprogram.
5. Explain in brief about the term Peripheral devices.
6. What is a Priority Interrupt?
7. Explain in brief about Data manipulation instructions.
8. Explain in brief about main memory.
9. Explain in brief about the concept of pipeline.
10. Explain in brief about arithmetic pipeline.

SECTION-B

11. Write a detailed note on Logic microoperations.
12. Explain in detail about memory reference instructions.
13. Write a detailed note on I/O Interface.
14. Difference between hardwired control unit and microprogrammed control unit.
15. Explain in brief about following :
 - a) Design of Accumulator Logic.
 - b) Serial communication.

SECTION-C

16. Write a detailed note on RISC and CISC architecture.
17. Explain the vector processor and array processors in detail.
18. Explain the following terms in detail :
 - a) Main memory
 - b) Associative memory
 - c) Virtual memory