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Total No. of Pages : 02

Total No. of Questions : 09

**B.Tech.(EIE) (2011 & Onwards) (Sem.-5)**  
**MICRO PROCESSORS AND ITS APPLICATIONS**

Subject Code : EC-309

Paper ID : [A0365]

Time : 3 Hrs.

Max. Marks : 60

**INSTRUCTION TO CANDIDATES :**

1. **SECTION-A** is **COMPULSORY** consisting of **TEN** questions carrying **TWO** marks each.
2. **SECTION-B** contains **FIVE** questions carrying **FIVE** marks each and students have to attempt any **FOUR** questions.
3. **SECTION-C** contains **THREE** questions carrying **TEN** marks each and students have to attempt any **TWO** questions.

**SECTION-A****1. Answer briefly :**

- a) Why interfacing is needed for I/O devices?
- b) Define program counter.
- c) List the hardware interrupts in 8085.
- d) What is masking and why is it required?
- e) What is the need for timing diagram?
- f) Explain the instruction format of 8085.
- g) Differentiate I/O mapped and Memory mapped I/O.
- h) Specify the number of registers in 2K memory chip.
- i) Specify the opcode and operand in the instruction MOV H,L.
- j) Differentiate ADC and DAC.

### SECTION-B

2. Compare CALL and JMP instruction of 8085.
3. Discuss the S/H circuit operation.
4. Sketch the internal block diagram of 8086.
5. Find the Hex code for the following instructions and show the order of entering the codes in memory.
  - a) STA 2050H.
  - b) JNZ 2070H.
6. Explain microprocessor based Software development systems.

### SECTION-C

7. Draw and explain the control word format for I/O mode of 8255.
8. Draw and explain the internal block diagram of 8085.
9. Write short notes :
  - a) Assemblers and cross assemblers.
  - b) Zilog Z80.