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Total No. of Pages : 02

Total No. of Questions : 07

B.Sc.(IT) (2013 & 2014) (Sem.-3)
COMPUTER SYSTEM ARCHITECTURE
Subject Code : BS-201
Paper ID : [B0409]

Time : 3 Hrs.

Max. Marks : 60

INSTRUCTION TO CANDIDATES :

1. SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.
2. SECTION-B contains SIX questions carrying TEN marks each and a student has to attempt any FOUR questions.

SECTION-A**Q1) Answer briefly :**

- a) Explain the steps involved in an instruction cycle.
- b) What do you mean by page fault?
- c) Write note on handshaking.
- d) Difference between internal and external interrupts.
- e) What do you mean by Zero Address Instruction?
- f) Differentiate between vectored and non-vectored interrupts.
- g) Define DMA.
- h) What is a DMA controller?
- i) What is the need of an interrupt?
- j) What is Program Counter?

SECTION-B

- Q2) Define Cache Memory. Explain the different modes of address mapping in Cache Memory.
- Q3) What is virtual memory and how can it be implemented using paging?
- Q4) Explain the various instruction types in detail.
- Q5) What is computer system architecture? Describe in detail architecture classification schemes.
- Q6) Explain page replacement algorithm with help of an example.
- Q7) Write a note on DMA Controller and IOP using block diagram.

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