Roll No. ☐☐☐☐☐☐☐☐☐☐☐☐☐   **Total No. of Pages : 02**

Total No. of Questions : 07

B.Sc.(IT) (2015 & Onward)   (Sem.–3)
# COMPUTER SYSTEM ARCHITECTURE
Subject Code : BSIT-301
Paper ID : [74059]

Time : 3 Hrs.   **Max. Marks : 60**

INSTRUCTIONS TO CANDIDATES :
1. SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.
2. SECTION-B contains SIX questions carrying TEN marks each and students have to attempt any FOUR questions.

SECTION-A

1. **Answer briefly :**

   a) What is Flynn's classification?

   b) Discuss the Stored Program concept.

   c) What is the use of Data bus?

   d) Define DMA.

   e) What do you mean by a zero address instruction?

   f) What do you mean by Asynchronous Data Transfer?

   g) Discuss the concept of Handshaking in data transfer.

   h) Define Cache memory.

   i) What is an I/O processor?

   j) What is Android architecture?

SECTION-B

2) Discuss the various types of micro operations.

3) Explain the instruction cycle of a computer.

4) Explain the various addressing modes available for executing instructions.

5) Explain the importance of interrupts. How many types of interrupts are there? Explain the interrupt cycle.

6) What is the difference between a microprocessor and a microprogram? Explain the difference between hardwired control and microprogrammed control.

7) What do you mean by memory hierarchy? Discuss in detail about various types of memories.