**Roll No.** ⬚⬚⬚⬚⬚⬚⬚⬚⬚⬚⬚⬚⬚          **Total No. of Pages : 02**

**Total No. of Questions : 09**

### M.Sc.(Computer Science) (2015 & Onwards)    (Sem.–2)
## COMPUTER ORGANISATION
### Subject Code : MSC-202
### Paper ID : [A2571]

**Time : 3 Hrs.**                                    **Max. Marks : 60**

**INSTRUCTION TO CANDIDATES :**

1. **SECTIONS-A, B, C & D contains TWO questions each carrying TEN marks each and students has to attempt any ONE question from each SECTION.**
2. **SECTION-E is COMPULSORY consisting of TEN questions carrying TWENTY marks in all.**
3. **Use of non-programmable scientific calculator is allowed.**

### SECTION - A

Q1)  Elaborate the need of Instruction set and Instruction codes.

Q2)  Discuss memory reference instructions in detail.

### SECTION-B

Q3)  Discuss various Addressing modes in detail with example.

Q4)  a)  Discuss the data transfer mechanism and its manipulation.

   b)  Differentiate between Hard wired control and Micro programmed control unit.

### SECTION-C

Q5)  Discuss Pipeline processing with its types in detail.

Q6)  a)  Write short note on input-output processors.

   b)  Write short note on Priority interrupt.

### SECTION-D

Q7)  Explain Address mapping using paging.

Q8)  Describe the use of interconnection structures.

**SECTION-E**

**Q9) Answer briefly :**

a) Explain Logic micro operations.

b) Write short note on Match Logic.

c) Explain direct memory address.

d) Explain BCD Adder.

e) Explain Advantages of memory organization.

f) What do you mean by Memory page table?

g) Explain cache coherency.

h) Discuss cache initialization.

i) Explain Bus Interfaces.

j) Discuss Static RAM.