

R09

Code: 9A02705

B.Tech IV Year I Semester (R09) Supplementary Examinations, May 2013

HIGH VOLTAGE ENGINEERING

(Electrical and Electronics Engineering)

Time: 3 hours

Max Marks: 70

Answer any FIVE questions
All questions carry equal marks

- Classify different voltage levels and mention need for going high voltage generation.
 - Write short notes on electrostatic separation.
- State and explain Paschen's law and derive for $(V_b)_{\min}$, $(Pd)_{\min}$
 - Explain the breakdown procedure in pure liquids.
- Explain intrinsic breakdown in solids.
 - Explain thermal breakdown in solids.
- Explain Cockroft – Walton voltage multiplier circuit for generating high voltages.
 - A Cockroft-Walton type voltage multiplier has 8 stages with capacitances all equal to $0.05 \mu f$. The supply Transformer secondary voltage is 125 KV at a frequency of 150 Hz. If the load current to be supplied is 5 mA.
Calculate: (i) % of Ripple. (ii) Regulation. (iii) Optimum no of stages.
- Explain MARX circuit.
 - Write short notes on Impulse current generation.
- How high D.C, A.C and impulse voltages are measured using sphere gap. Mention the factors effecting spark over voltage of sphere gaps.
- Define the terms:
 - Electrical discharge.
 - Partial discharge.
 - Discharge extinction voltage.
 - Discharge inception voltage.
 - How the partial discharges are measured using straight detectors?
- Explain Impulse test on power transformers.
 - Explain various tests conducted on cables.
