Code: ICT 9A05504

ICT

B.Tech IV Year I Semester (R09) Supplementary Examinations, May 2013 **COMPILER DESIGN**

(Computer Science and Engineering)

Time: 3 hours

Max Marks: 70

Answer any FIVE questions All questions carry equal marks *****

- 1 (a) What is a LEX tool? How LEX programs are used to perform lexical analysis?
 - (b) Explain procedure for obtaining the regular expression from finite automata.
- What are the limitations of recursive descent parser? 2 (a)
 - (b) Construct recursive descent parser for the following grammar of regular expressions.

 $E \rightarrow E + T | T$ T→TF|F $F \rightarrow F^*|a|b$

- 3 (a) How to identify the operator precedence relations from associativity and precedence?
 - Explain about the construction of precedence functions. (b)
- 4 (a) What is a type checker? How does it work?
 - Write a short notes on static and dynamic type checking. (b)
- Explain dynamic scope. Differentiate between deep access and shallow access. 5 (a)
 - Discuss various parameter passing techniques. (b)
- 6 (a) What do you mean by DAG? Explain the algorithm for constructing a DAG with example.
 - Why are guadruples preferred over triples in an optimizing compiler? Explain. (b)
- Define flow graph. Show the relationship between DAG and flow graph. 7 (a)
 - (b) Explain in detail about dead code elimination.
- 8 Generate code for the following C program.

```
main()
{
         int a = 10;
         int k;
         for(k = 1; k < = 10; k + +)
         a = a^{k+k/a}
}
```

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