Code I	No: V3126	7	Set No: 1		
III B.Tech. I Semester Supplementary Examinations, November/December - 2012					
Time	(Common to Computer Science and Enginee : 3 Hours Answer any FIVE Q All Questions carry eq *****	ring & Information Technology Max uestions ual marks) Marks: 80		
1	a) What is meant by a software bug? Discussb) What are the requirements, features and fu	in detail the consequences of nctionality of bugs?	bugs. (8+8)		
2	a) What is meant by program's control flow?b) What is a flowgraph? Discuss its elements	How is it helpful for path test and notations.	ting? (8+8)		
3	a) Explain about the transaction flow testing b) Explain the APU and ACU strategy of dat	strategies.	(8+8)		
4	a) Give the schematic representation of domab) Explain about nice and ugly domains.	in testing and explain it.	(8+8)		
5	a) Discuss in brief the applications of paths.b) State Huang's theorem and explain its use	with example.	(8+8)		
6	a) What is a decision table? Discuss the roleb) Explain about the ambiguities and contrad	of decision table in a test case ictions in the specifications.	design. (8+8)		
7	a) Discuss the types of bugs in state graph.b) Explain with example how to convert a sport	ecification into a state graph.	(8+8)		
8	 a) Discuss about the performance testing of a b) Explain cross-term reduction and node-ter **** 	database application. m reduction optimization. *	(8+8)		

1 of 1

Code	No: V3126	Set No: 2			
III B.Tech. I Semester Supplementary Examinations, November/December - 2012 SOFTWARE TESTING METHODOLOGIES (Common to Computer Science and Engineering & Information Technology) Time: 3 Hours					
	Answer any FIVE Questions All Questions carry equal marks *****				
1	What are structural bugs, coding bugs, data bugs, and system bugs? these bugs can be caught.	Discuss how (16)			
2	a) What is meant by path sensitization? Discuss clearly the heuristic p sensitizing paths.b) Discuss the followingi) Predicatesii) Predicate expression	(8+8)			
3	a) State and explain various transaction flow junctions and mergers.b) Compare the path flow and data flow testing strategies.	(8+8)			
4	a) List and explain the different domain boundaries.b) Explain about the testing of one dimensional domains.	(8+8)			
5	 Explain the following. a. Distributive laws. b. Absorption rules c. Path sums d. Path products. 				
6	a) Explain the expansion of the immaterial cases in the decision table.b) Explain prime implicant, sum-of-product, product-of-sum forms.	(8+8)			
7	a) What are the good state and bad state graphs? How they differ?b) Write the design guidelines for building the finite state machine into control of the state	ode. (8+8)			
8	 a) Illustrate the application of node reduction algorithm. b) Discuss about the WinRunner tool . 	(8+8)			

1 of 1

Code	No: V3126	R07		Set No: 3	
III B.Tech. I Semester Supplementary Examinations, November/December - 2012 SOFTWARE TESTING METHODOLOGIES					
Tim	(Common to Computer Science and ne: 3 Hours Answer any All Questions	Engineering & Infor FIVE Questions s carry equal marks	mation Technolog Ma	y) x Marks: 80	
1	a) What is meant by functional testinb) Discuss in detail how the consequence	g and structural test ences of bugs are m	ting? Discuss its deasured?	differences. (8+8)	
2	a) Discuss about coincidental correctb) What is meant by statement cover	ness with example. age and branch cove	erage?	(8+8)	
3	a) What are the transactional flows?b) Explain different types of data flows?	Why transactional f w anomalies and da	lows are structure ata flow anomaly	ed? state graphs. (8+8)	
4	a) Discuss about variations, tools andb) What is the purpose of domain testit?a) Explain about lower path count ar	l effectiveness of do sting? What are the	omain testing. domain bugs? H	ow to test for (8+8)	
5	a) Explain about lower pair count arb) Explain the following.i) Distributive laws.ii) Abs	orption rules		(8+8)	
6	a) Explain the procedure for specific b) Explain about ambiguities and con	ation validation usin ntradictions in the sj	ng KV charts.	(8+8)	
7	a) What are the principles of state teb) Explain about essential and inesse	esting? Explain its a ntial finite state beh	idvantages and d avior.	isadvantages. (8+8)	
8	a) Discuss the relative merits and der b) Write an algorithm for node reduc	nerits of different g tion. *****	raph matrix repre	esentations. (8+8)	

Code	No: V3126	Set No: 4			
III B.Tech. I Semester Supplementary Examinations, November/December - 2012 SOFTWARE TESTING METHODOLOGIES					
	(Common to Computer Science and Engineering & Information Technolog	y)			
Tim	ie: 3 Hours Ma	x Marks: 80			
	Answer any FIVE Questions				
	All Questions carry equal marks *****				
1	a) Explain how software testing ensures the quality of software.	(0 - 0)			
	b) Differentiate between testing and debugging.	(8+8)			
2	a) What is meant by program's control flow? How is it helpful for path testing?b) Explain the conversion of a multi-entry routine to an equivalent single entry routine with example. (8+8)				
3	a) What is data flow testing? Explain its strategies.b) Explain the transactional flow testing with an example.	(8+8)			
4	a) Explain about domains and testability.b) How ugly domains are treated by programmers and testers?	(8+8)			
5	a) Discuss about path sum and path product.b) Write the steps involved in node reduction procedure.	(8+8)			
6	a) What is a decision table? What are its applications?b) What is a KV chart? What for it is used?	(8+8)			
7	a) Write the design guide lines for building the finite state machine into cb) Explain about the state testing and testability tips.	code. (8+8)			
8	 a) What operations does a toolkit consist for the representation of graphs b) Explain cross-term reduction and node-term reduction optimization. 	? (8+8)			

1 of 1