Code: R7310502

R7

B.Tech III Year I Semester (R07) Supplementary Examinations, May 2013
SOFTWARE TESTING METHODOLOGIES
(Common to CSE and IT)

Time: 3 hours   Max Marks: 80

Answer any FIVE questions
All questions carry equal marks
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1   What is the relationship between modularity and efficiency? Explain the real world context characterized by model project.

2   Explain the following:
   (a)   Achievable and unachievable paths.
   (b)   Traversal marker form for path instrumentation.

3   What is transaction flow and neatly explain its implementation?

4 (a)   What are the restrictions of domain testing? Explain briefly them.
   (b)   Discuss complete domain boundaries.

5 (a)   Write about the basic concepts of a path product with example.
   (b)   Write a short note for finding the paths in a flow graph.

6 (a)   What are KV charts give examples of KV charts for three variables?
   (b)   Give the testing strategies of KV charts.

7   Define state transition testing and explain how it is useful in language development.

8 (a)   Write about loops in matrix representation.
   (b)   Explain partitioning algorithm with example.

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